

Sheet 1 of 11

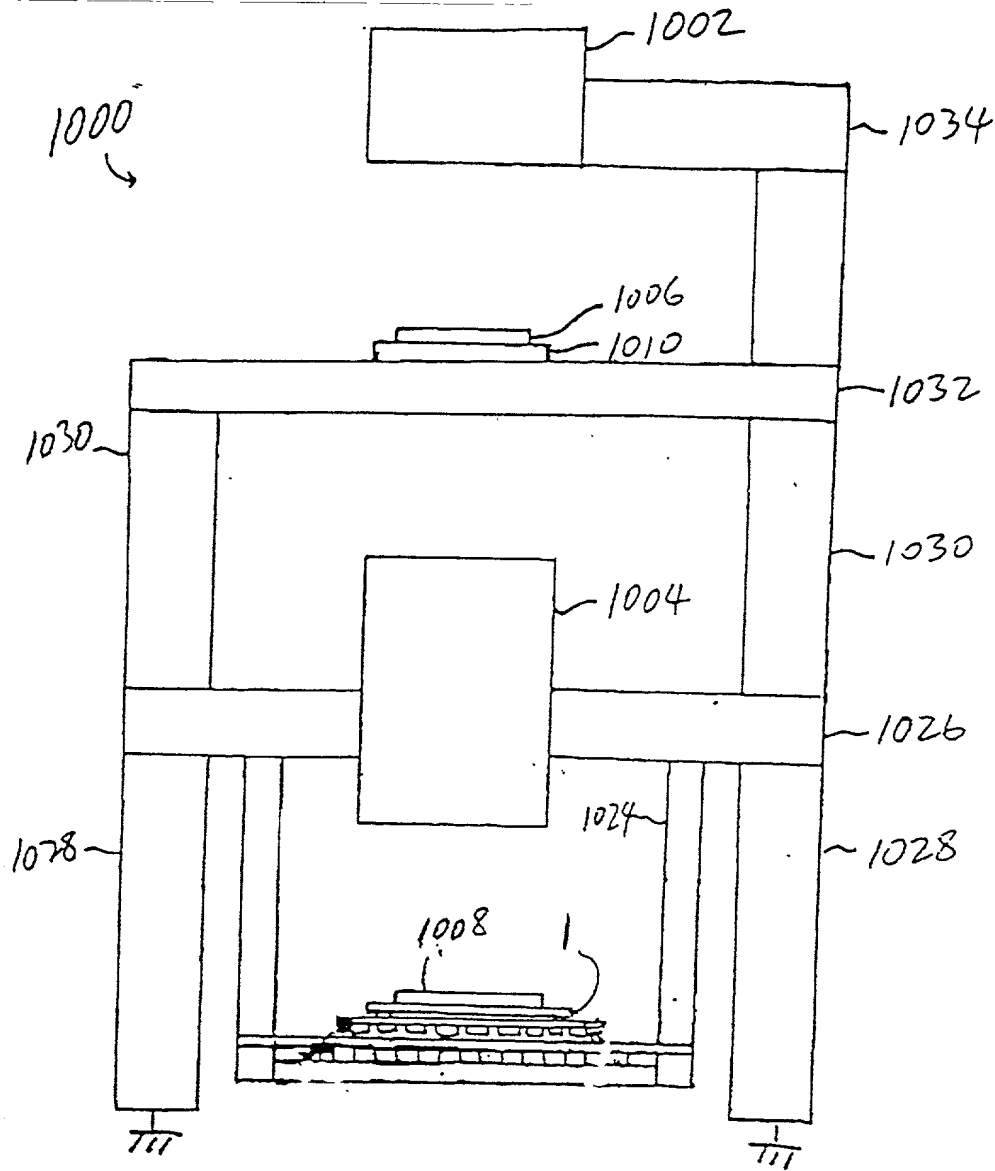
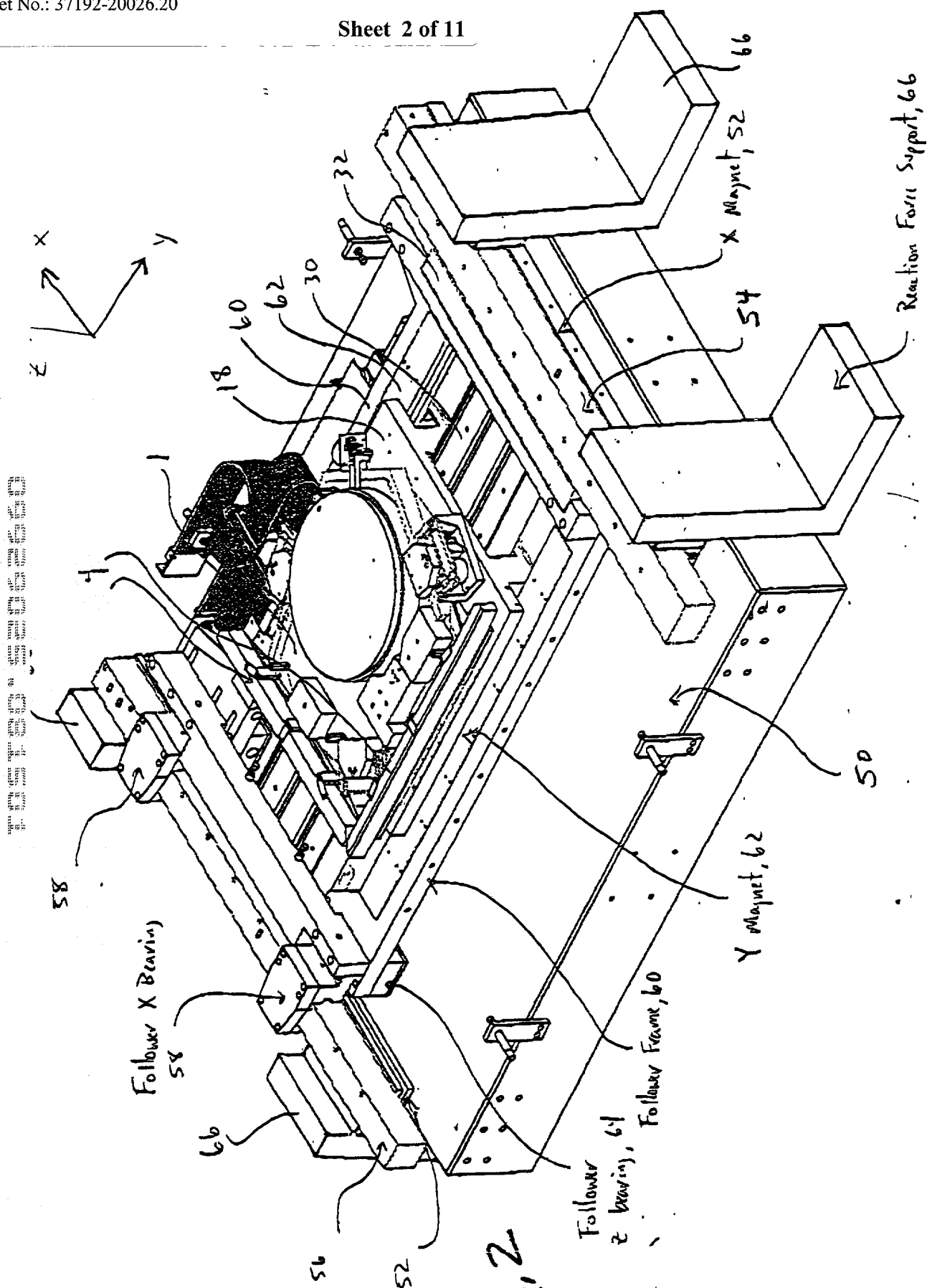


Fig. 1



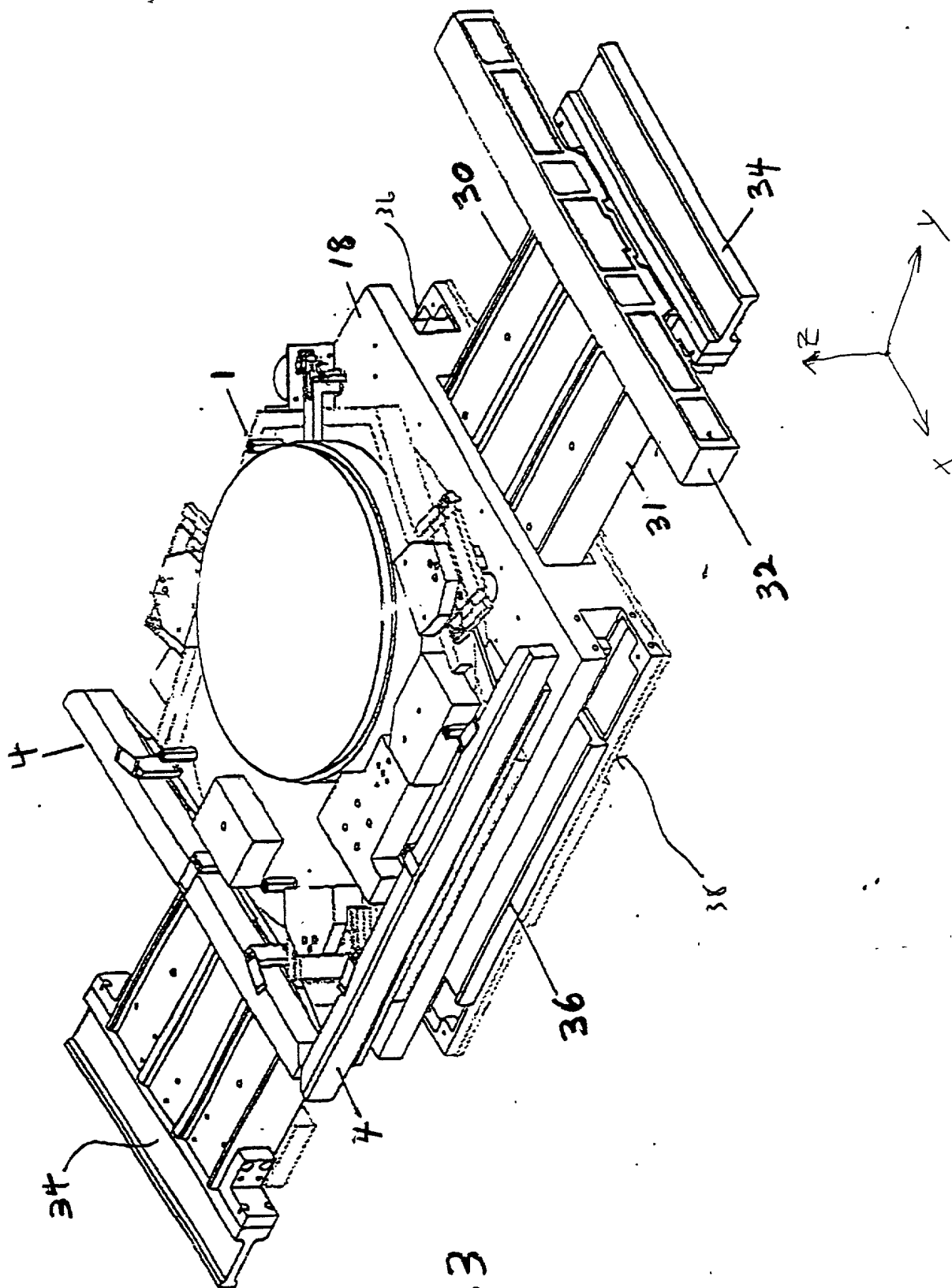
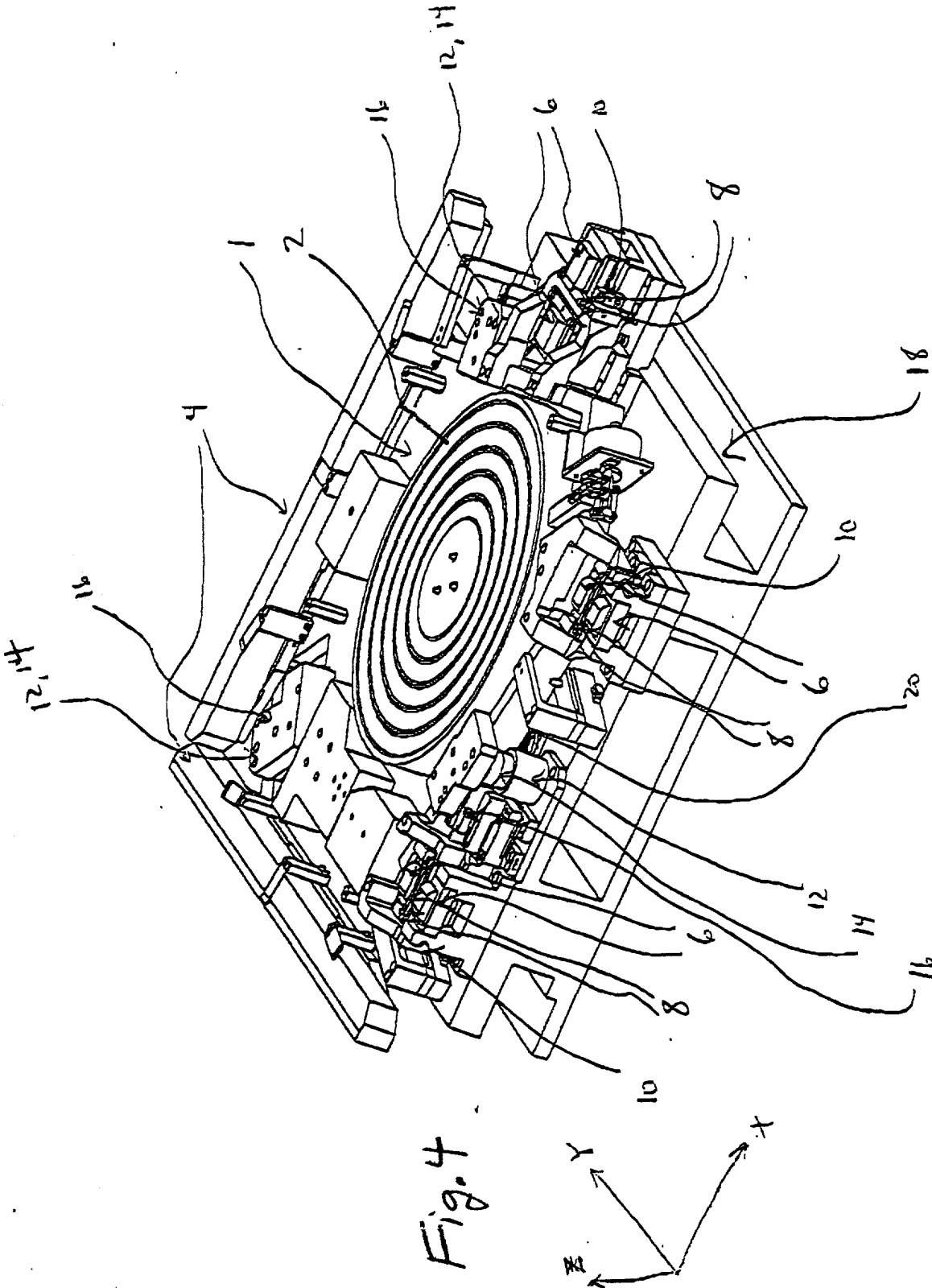


Fig. 3

FIG. 4 is a perspective view of the wafer stage 100 in a second position. The wafer stage 100 is shown in a second position, which is a position where the wafer stage 100 is tilted. The wafer stage 100 is shown in a second position, which is a position where the wafer stage 100 is tilted. The wafer stage 100 is shown in a second position, which is a position where the wafer stage 100 is tilted.



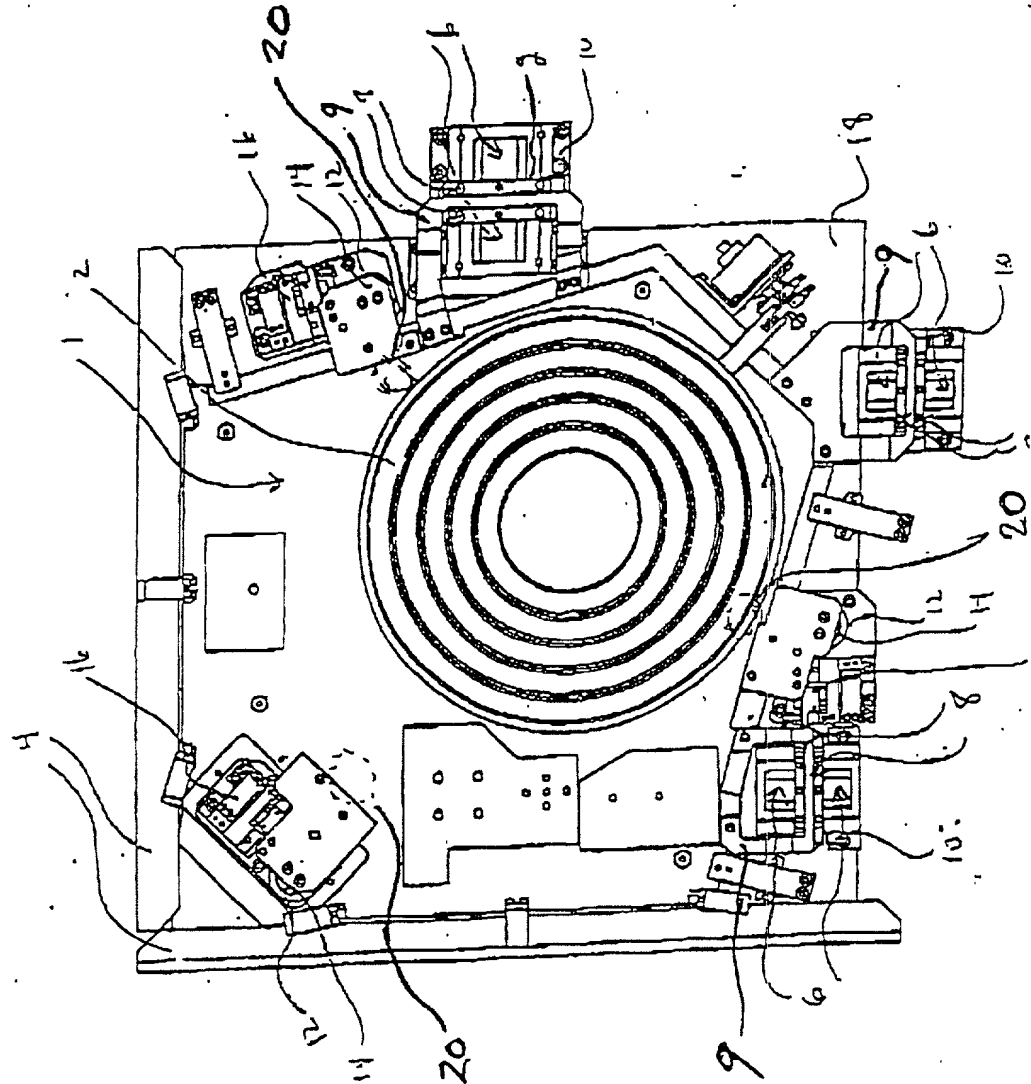
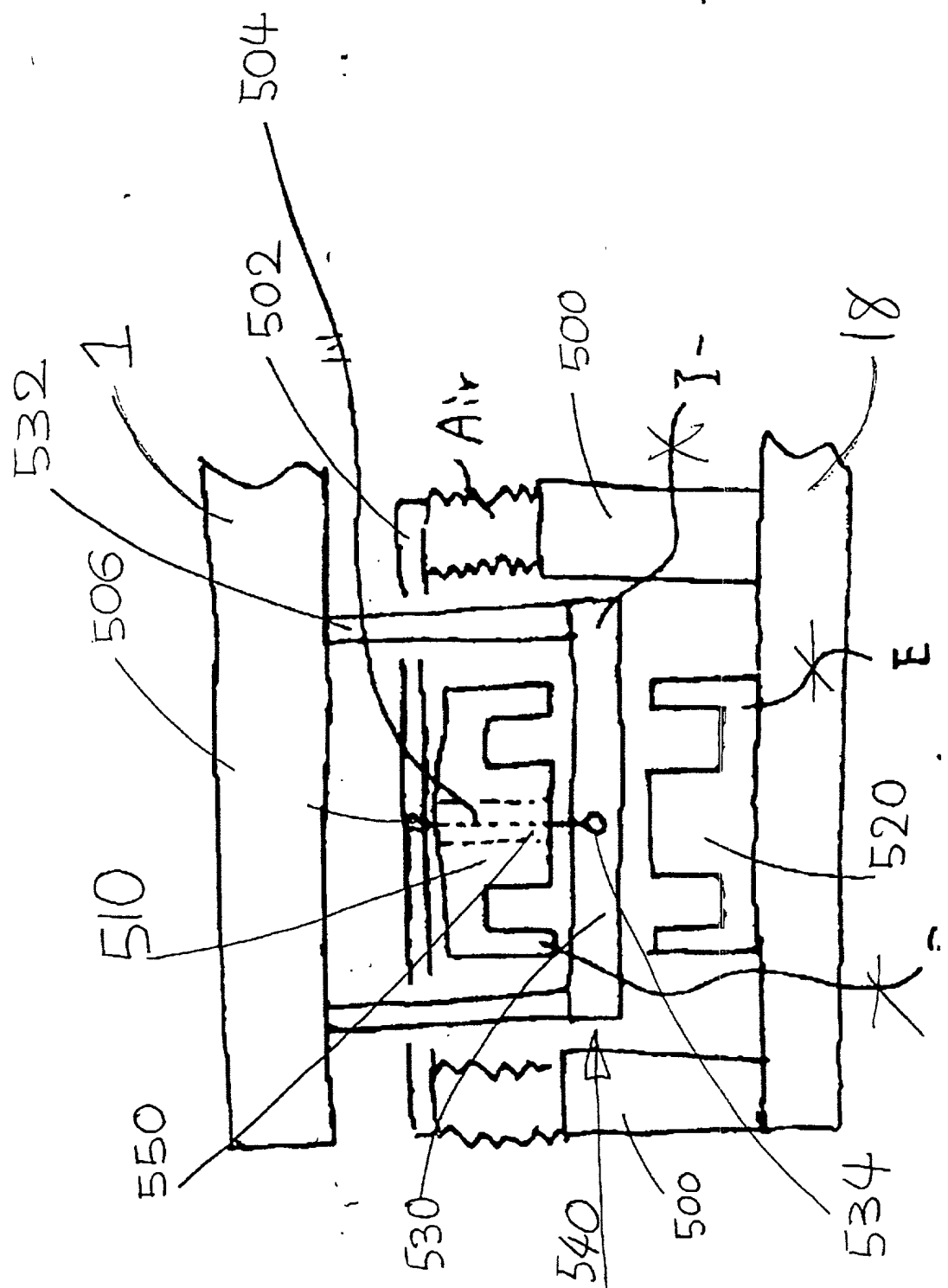


Fig. 5A

★
FIG. 5B



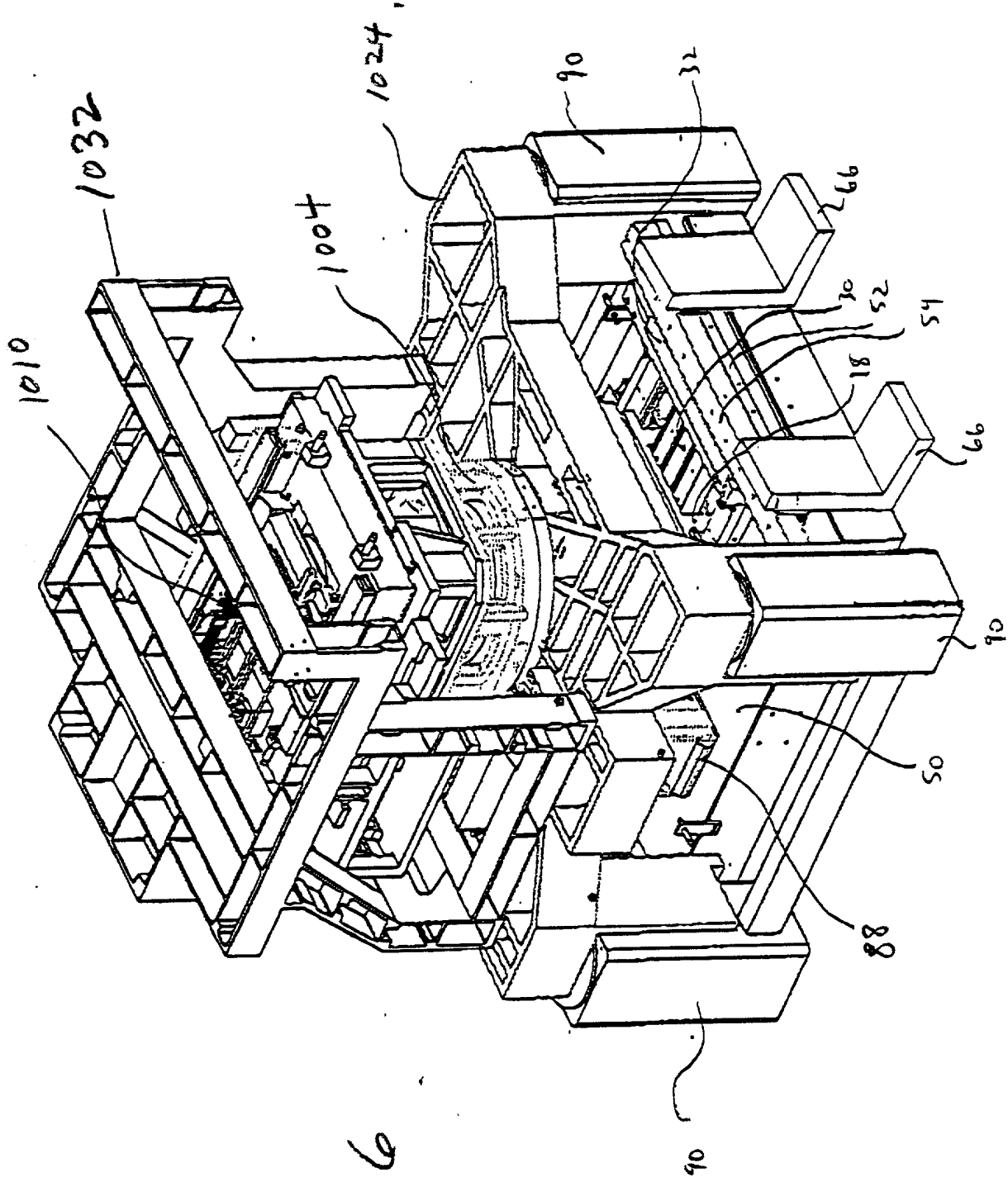


Fig. 6

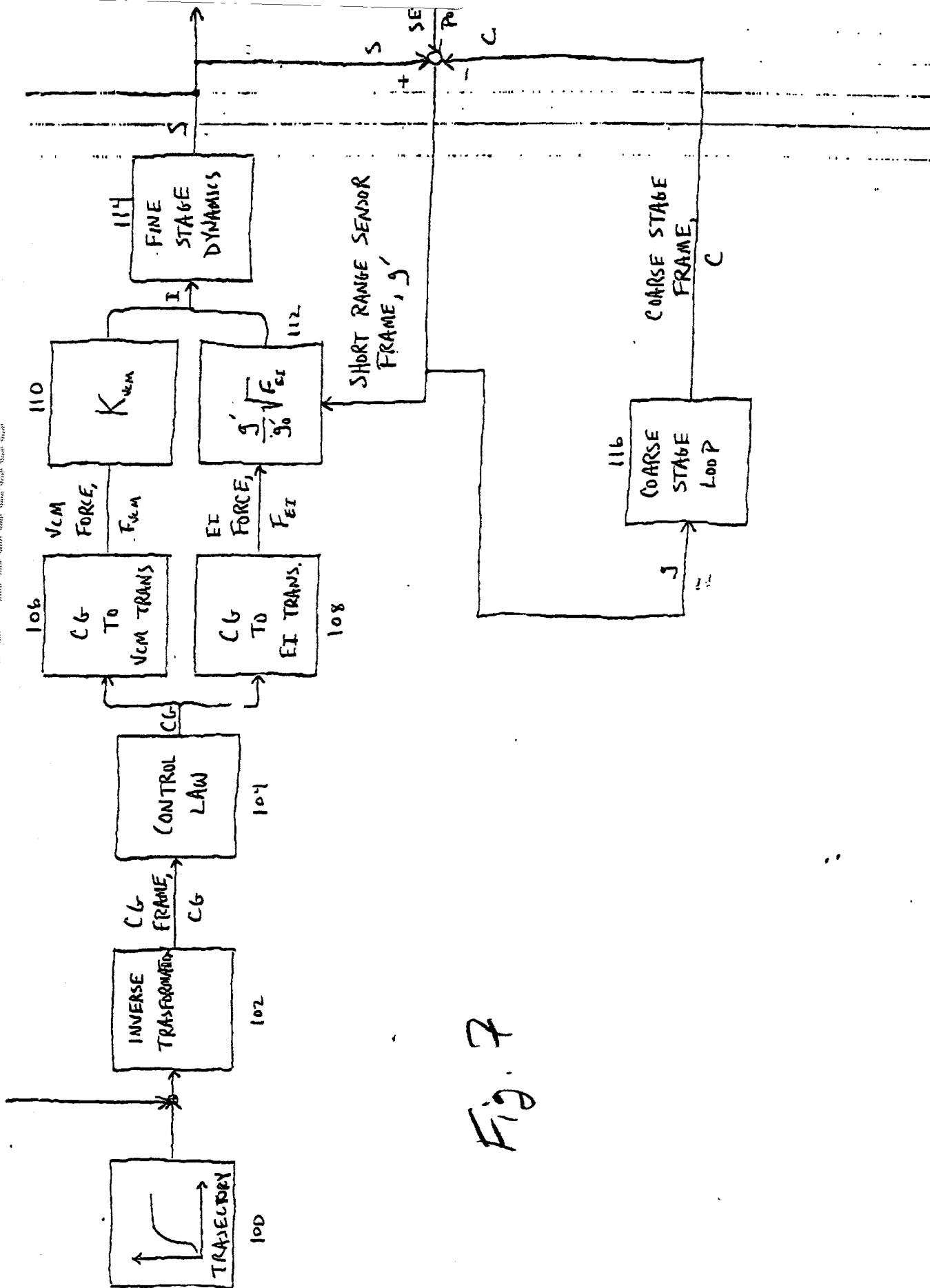
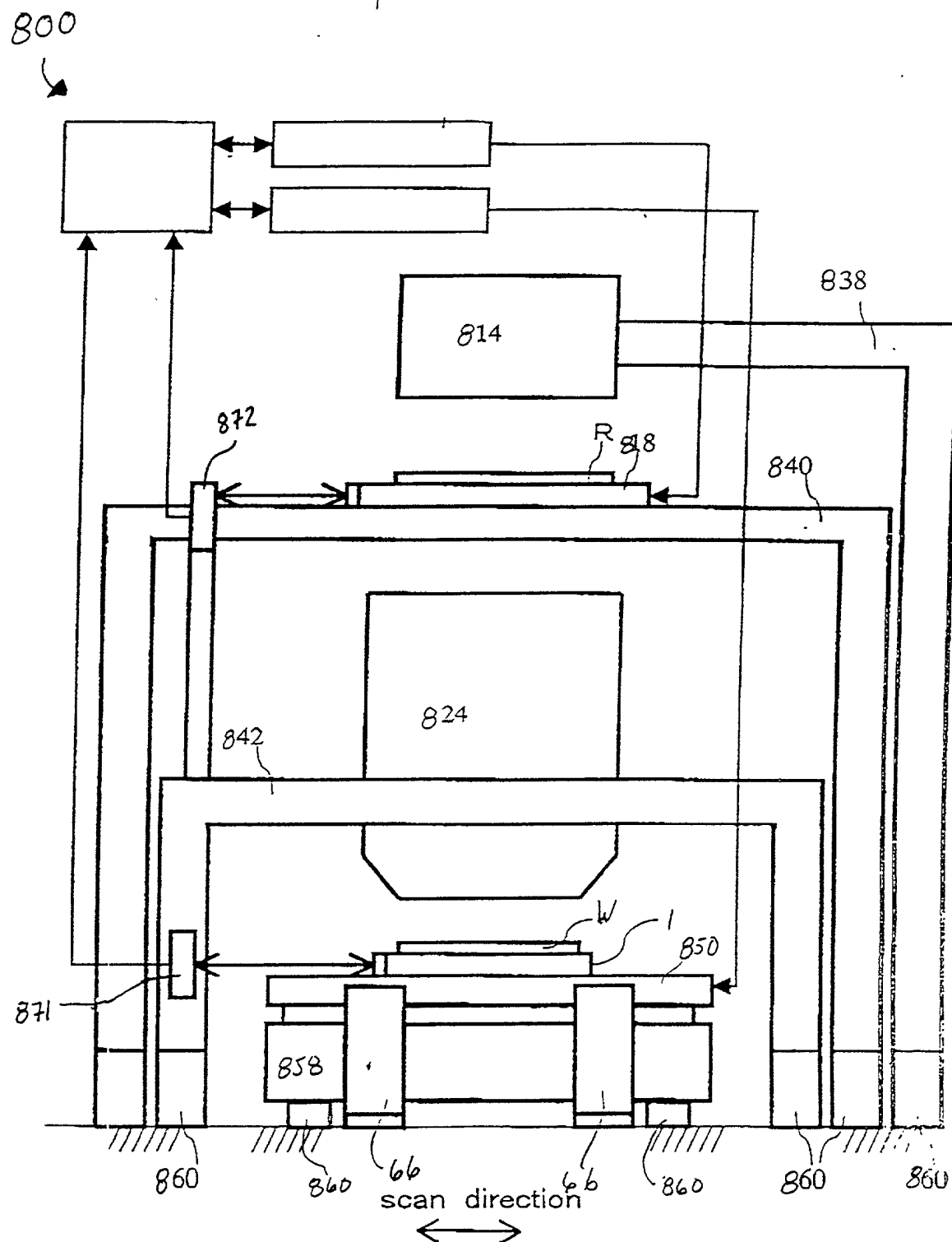


Fig. 7

FIG. 8



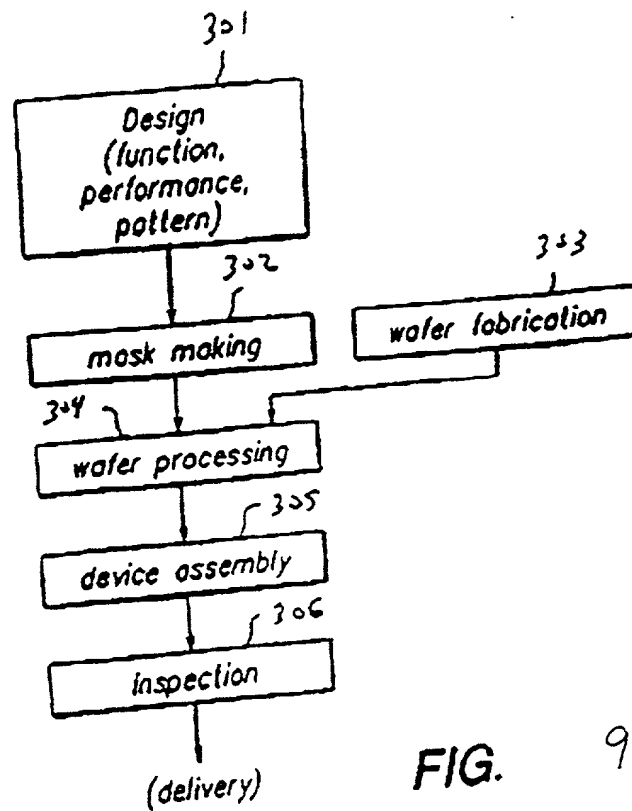


FIG. 9

FIG. 10.

